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Using simple language, this text explains the properties of light, its interaction with matter, and how it is used to develop optical components such as filters and multiplexers that have applications in optical communications. The text also introduces the evolving dense wavelength division multiplexing (DWDM) technology and communications systems.

CCIE Routing and Switching v5.0 Official Cert Guide Library, Fifth Edition Assessment, review, and practice package for the CCIE R&S v5.0 exams Includes CCIE Routing and Switching v5.0 Official Cert Guide, Volume 1, Fifth Edition, and CCIE Routing and Switching v5.0 Official Cert Guide, Volume 2, Fifth Edition. This is the eBook version of the print title. The eBook edition does not provide access to the test engine software or the practice tests that accompany the print book These official study guides help you master all the topics on the CCIE R&S v5.0 exams, including Virtual LANs and VLAN Trunking Spanning Tree Protocol (STP) IP services (ARP, NTP, DHCP, NAT, SNMP, NetFlow, and more) RIPv2 and RIPv3 EIGRP OSPFv2 and v3 IS-IS Route redistribution, route summarization, default routing, and performance routing BGP operations and routing policies QoS WANs IP Multicast Device and network security and tunneling technologies MPLS CCIE Routing and Switching v5.0 Official Cert Guide Library, Fifth Edition from Cisco Press enables you to succeed on the exam the first time and is the only self-study resource approved by Cisco. In the two books included in this package, expert instructors Narbik Kocharians, Peter Pal ú ch, and Terry Vinson share preparation hints and test-taking tips, helping you identify areas of weakness and improve both your conceptual knowledge and hands-on skills. The first volume covers LAN switching, IP networking, and IP IGP routing topics. The second volume covers IP BGP routing, quality of service (QoS), wide area networks, IP multicast, network security, and Multiprotocol Label Switching (MPLS) topics. Together, these two books cover all the topics on the new v5.0 blueprint. This complete study package includes A test-preparation routine proven to help you pass the exams “ Do I Know This Already? ” quizzes, which enable you to decide how much time you need to spend on each section Chapter-ending exercises, which help you drill on key concepts you must know thoroughly A final preparation chapter, which guides you through tools and resources to help you craft your review and test-taking strategies Study plan suggestions and templates to help you organize and optimize your study time Well regarded for its level of detail, study plans, assessment features, challenging review questions and exercises, this official study guide helps you master the concepts and techniques that ensure your exam success. CCIE Routing and Switching v5.0 Official Cert Guide Library, Fifth Edition is part of a recommended learning path from Cisco that includes simulation and hands-on training from authorized Cisco Learning Partners and self-study products from Cisco Press. To find out more about instructor-led training, e-learning, and hands-on instruction offered by authorized Cisco Learning Partners worldwide, please visit www.cisco.com. This volume is part of the Official Cert Guide series from Cisco Press. Books in this series provide officially developed exam preparation materials that offer assessment, review, and practice to help Cisco Career Certification candidates identify weaknesses, concentrate their study efforts, and enhance their confidence as exam day nears.

For more than 20 years, Network World has been the premier provider of information, intelligence and insight for network and IT executives responsible for the digital nervous systems of large organizations. Readers are

responsible for designing, implementing and managing the voice, data and video systems their companies use to support everything from business critical applications to employee collaboration and electronic commerce.

Email Security with Cisco IronPort thoroughly illuminates the security and performance challenges associated with today ' s messaging environments and shows you how to systematically anticipate and respond to them using Cisco ' s IronPort Email Security Appliance (ESA). Going far beyond any IronPort user guide, leading Cisco expert Chris Porter shows you how to use IronPort to construct a robust, secure, high-performance email architecture that can resist future attacks. Email Security with Cisco IronPort presents specific, proven architecture recommendations for deploying IronPort ESAs in diverse environments to optimize reliability and automatically handle failure. The author offers specific recipes for solving a wide range of messaging security problems, and he demonstrates how to use both basic and advanced features- – including several hidden and undocumented commands. The author addresses issues ranging from directory integration to performance monitoring and optimization, and he offers powerful insights into often-ignored email security issues, such as preventing “ bounce blowback. ” Throughout, he illustrates his solutions with detailed examples demonstrating how to control ESA configuration through each available interface. Chris Porter, Technical Solutions Architect at Cisco, focuses on the technical aspects of Cisco IronPort customer engagements. He has more than 12 years of experience in applications, computing, and security in finance, government, Fortune® 1000, entertainment, and higher education markets.

- Understand how the Cisco IronPort ESA addresses the key challenges of email security
- Select the best network deployment model for your environment, and walk through successful installation and configuration
- Configure and optimize Cisco IronPort ESA ' s powerful security, message, and content filtering
- Understand the email pipeline so you can take full advantage of it – and troubleshoot problems if they occur
- Efficiently control Cisco IronPort ESA through its Web User Interface (WUI) and command-line interface (CLI)
- Implement reporting, monitoring, logging, and file management
- Integrate Cisco IronPort ESA and your mail policies with LDAP directories such as Microsoft Active Directory
- Automate and simplify email security administration
- Deploy multiple Cisco IronPort ESAs and advanced network configurations
- Prepare for emerging shifts in enterprise email usage and new security challenges

This security book is part of the Cisco Press® Networking Technology Series. Security titles from Cisco Press help networking professionals secure critical data and resources, prevent and mitigate network attacks, and build end-to-end self-defending networks.

Ethernet Networks, Fourth Edition, provides everything you need to know to plan, implement, manage and upgrade Ethernet networks. * Improve your skills in employing Ethernet hubs, switches, and routers. * Learn how to set up and operate a wireless Local Area Network (LAN). * Discover how to extend a wired Ethernet via wireless LANs. * Understand cabling standards and the role of NEXT (Near End Crosstalk), FEXT (Far End Crosstalk) and other transmission parameters. * Profit from Gilbert Held's tips and tricks on enhancing security ... and much more. This indispensable resource features up-to-date coverage of: * Wireless Ethernet (IEEE802.11 standards) * 10Gbps Ethernet * Firewalls in both a wired and wireless environment * The operation of new versions of Windows(r) on Ethernet LANs * The use of LAN switches at and above layer 2 in the ISO reference model * Copper and fiber optic cable to transport high speed Ethernet Network planners, administrators, and system engineers working with Ethernet networks will find Ethernet Networks, Fourth Edition, an invaluable tool for implementing, updating, and managing their networks.

Computer Architecture: A Quantitative Approach, Sixth Edition has been considered essential reading by instructors, students and practitioners of computer design for over 20 years. The sixth edition of this classic textbook from Hennessy and Patterson, winners of the 2017 ACM A.M. Turing Award recognizing contributions of lasting and major technical importance to the computing field, is fully revised with the latest developments in processor and system architecture. The text now features examples from the RISC-V (RISC Five) instruction set architecture, a modern RISC instruction set developed and designed to be a free and openly adoptable standard. It also includes a new chapter on domain-specific architectures and an updated chapter on warehouse-scale computing that features the first public information on Google's newest WSC. True to its original mission of demystifying computer architecture, this edition continues the longstanding tradition of focusing on areas where the most exciting computing innovation is happening, while always keeping an emphasis on good engineering design. Winner of a 2019 Textbook Excellence Award (Texty) from the Textbook and Academic Authors Association Includes a new chapter on domain-specific architectures, explaining how they are the only path forward for improved performance and energy efficiency given the end of Moore ' s Law and Dennard scaling Features the first publication of several DSAs from industry Features extensive updates to the chapter on warehouse-scale computing, with the first public information on the newest Google WSC Offers updates to other chapters including new material dealing with the use of stacked DRAM; data on the performance of new NVIDIA Pascal GPU vs. new AVX-512 Intel Skylake CPU; and extensive additions to content covering multicore architecture and organization Includes "Putting It All Together" sections near the end of every chapter, providing real-world technology examples that demonstrate the principles covered in each chapter Includes review appendices in the printed text and additional reference appendices available online Includes updated and improved case studies and exercises ACM named John L. Hennessy and David A. Patterson, recipients of the 2017 ACM A.M. Turing Award for pioneering a systematic, quantitative approach to the design and evaluation of computer architectures with enduring impact on the microprocessor industry

This best-selling title, considered for over a decade to be essential reading for every serious student and practitioner of computer design, has been updated throughout to address the most important trends facing computer designers today. In this edition, the authors bring their trademark method of quantitative analysis not only to high performance desktop machine design, but also to the design of embedded and server systems. They have illustrated their principles with designs from all three of these domains, including examples from consumer electronics, multimedia and web technologies, and high performance computing. The book retains its highly rated features: Fallacies and Pitfalls, which share the hard-won lessons of real designers; Historical Perspectives, which provide a deeper look at computer design history; Putting it all Together, which present a design example that illustrates the principles of the chapter; Worked Examples, which challenge the reader to apply the concepts, theories and methods in smaller scale problems; and Cross-Cutting Issues, which show how the ideas covered in one chapter interact with those presented in others. In addition, a new feature, Another View, presents brief design examples in one of the three domains other than the one chosen for Putting It All Together. The authors present a new organization of the material as well, reducing the overlap with their other text, Computer Organization and Design: A Hardware/Software Approach 2/e, and offering more in-depth treatment of advanced topics in multithreading, instruction level parallelism, VLIW architectures, memory hierarchies, storage devices and network technologies. Also new to this edition, is the adoption of the MIPS 64 as the instruction set architecture. In addition to several online appendixes, two new appendixes will be printed in the book: one contains a complete review of the basic concepts of pipelining, the other provides solutions a selection of the exercises. Both will be invaluable to the student or professional learning on her own or in the classroom. Hennessy and Patterson continue to focus on fundamental techniques for designing real machines and for maximizing their cost/performance. * Presents state-of-the-art design examples including: * IA-64 architecture and its first implementation, the Itanium * Pipeline designs for Pentium III and Pentium IV * The cluster that runs the Google search engine * EMC storage systems and their performance * Sony Playstation 2 * Infiniband, a new storage area and system area network * SunFire 6800 multiprocessor server and its processor the UltraSPARC III * Trimedia TM32 media processor and the Transmeta Crusoe processor * Examines quantitative performance analysis in the commercial server market and the embedded market, as well as the traditional desktop market. Updates all the examples and figures with the most recent benchmarks, such as SPEC 2000. * Expands coverage of instruction sets to include descriptions of digital signal processors, media processors, and multimedia extensions to desktop processors. * Analyzes capacity, cost,

and performance of disks over two decades. Surveys the role of clusters in scientific computing and commercial computing. * Presents a survey, taxonomy, and the benchmarks of errors and failures in computer systems. * Presents detailed descriptions of the design of storage systems and of clusters. * Surveys memory hierarchies in modern microprocessors and the key parameters of modern disks. * Presents a glossary of networking terms.

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Pinson Mounds: Middle Woodland Ceremonialism in the Midsouth is a comprehensive overview and reinterpretation of the largest Middle Woodland mound complex in the Southeast. Located in west Tennessee about ten miles south of Jackson, the Pinson Mounds complex includes at least thirteen mounds, a geometric earthen embankment, and contemporary short-term occupation areas within an area of about four hundred acres. A unique feature of Pinson Mounds is the presence of five large, rectangular platform mounds from eight to seventy-two feet in height. Around A.D. 100, Pinson Mounds was a pilgrimage center that drew visitors from well beyond the local population and accommodated many distinct cultural groups and people of varied social stations. Stylistically nonlocal ceramics have been found in virtually every excavated locality, all together representing a large portion of the Southeast. Along with an overview of this important and unique mound complex, Pinson Mounds also provides a reassessment of roughly contemporary centers in the greater Midsouth and Lower Mississippi Valley and challenges past interpretations of the Hopewell phenomenon in the region.

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